

DPLL+ROBDD Derivation Applied to Inversion of Some Cryptographic Functions

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Inverting discrete functions

Consider polynomial time computable functions that form a family of type

$$f_n : \{0, 1\}^n \rightarrow \{0, 1\}^*,$$

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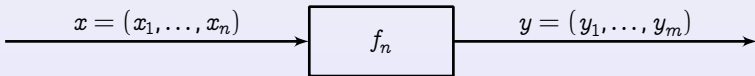
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- **Example (cryptanalysis of a keystream generator):**



Cryptanalysis problem: given a generator algorithm and a keystream $y = (y_1, \dots, y_m)$ one should find an initial sequence $x = (x_1, \dots, x_n)$.

Encoding to SAT

Let f_n be an arbitrary discrete function, $S(f_n)$ be a Boolean circuit representing f_n (e. g. over $\{\&, \neg\}$).

$$\begin{array}{ccc} \text{circuit} & \xrightarrow[\text{introducing auxiliary variables}]{\text{Tseitin transformations}} & \text{CNF} \\ S(f_n) & \xrightarrow{\quad} & \& C(G) \\ & & G \in S(f_n) \end{array}$$

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Then

$$C_y(f_n) = \left(\&_{G \in S(f_n)} C(G) \right) \cdot y_1^{\sigma_1} \cdot \dots \cdot y_m^{\sigma_m},$$

is a CNF encoding the inversion problem of the function f_n at point $y = (\sigma_1, \dots, \sigma_m)$. Here CNFs $C(G)$ encode gates G of $S(f_n)$ and

$$z^\sigma = \begin{cases} \bar{z}, & \text{if } \sigma = 0 \\ z, & \text{if } \sigma = 1 \end{cases}$$

A core-DPLL approach to inversion of discrete functions

- Approach: to restrict DPLL derivation to a set of variables denoting an input for $S(f_n)$.
- core-DPLL cannot polynomially simulate DPLL (even without clause learning and restarts)¹.
- The aim: to show that the use of core-DPLL provides a number of additional (or rather useful) technical capabilities while inverting discrete functions.

¹Järvisalo, M., Junttila, T.: Limitations of restricted branching in clause learning. *Constraints* 14(3), 325–356 (2009).

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Some other attempts to combine DPLL with BDDs:

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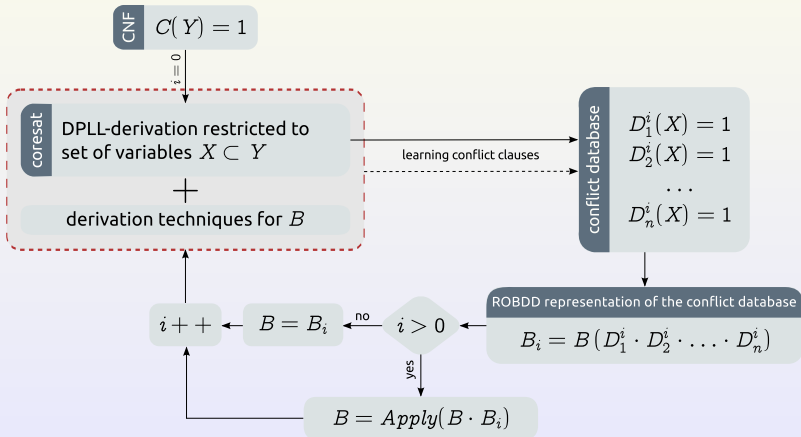
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- *No examples representing conflict clauses as ROBDDs and extending basic derivation algorithms to the ROBDD structure.*

A flowchart showing operation of the hybrid solver



ROBDD-based consequence

- Let $\Delta^1(x_i)$ be a set containing truth values of x_i , $i \in \{1, \dots, n\}$, defined by all the paths in ROBDD $B(f)$ from the root to terminal vertex "1".

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Definition:

The situation defined by conditions 1–2 is called a ROBDD-based consequence of a value of variable x_k .

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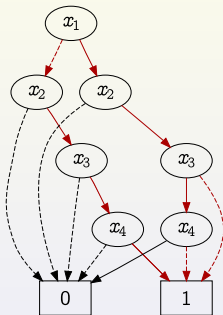


Figure: ROBDD representation of a function $x_2 \cdot (x_1 \oplus x_3 \cdot x_4)$ using the variable ordering

$x_1 \prec x_2 \prec x_3 \prec x_4$. We have a ROBDD-based consequence of variable x_2 ($x_2 = 1$) here because each path from the root to "1" passes through a vertex marked by x_2 and $|\Delta^1(x_2)| = 1$.

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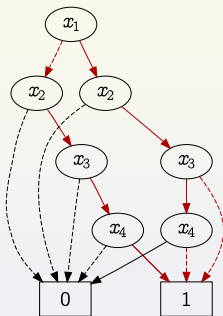


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Lemma. A ROBDD-based consequence of some variable x_k in $B(f)$ results in exactly one of the following:

- each vertex marked by x_k has "0" as the high-child;
- each vertex marked by x_k has "0" as the low-child.

ROBDD-based consequence

Theorem. For a ROBDD $B(f)$ and the values $x_{i_1} = \alpha_{i_1}, \dots, x_{i_m} = \alpha_{i_m}, m \leq n$, $\alpha_{i_j} \in \{0, 1\}, j \in \{1, \dots, m\}$, time complexity of the procedure² which substitutes given values into $B(f)$ and checks for ROBDD-based consequences of other variables is $O(|B(f)|)$.

²Similar mechanisms were described in [Damiano, R. F., Kukula, J. H.: Checking satisfiability of a conjunction of BDDs. In: 40th Design Automation Conference, DAC 2003, pp. 818–823 (2003)].

Avoiding BCP

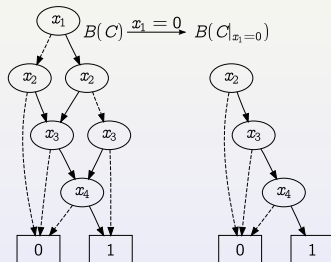
Corollary. If some substitution into $B(f)$ implies a ROBDD-based consequence of $x_k = \alpha_k$, $\alpha_k \in \{0, 1\}$ for some $x_k \in X$, then substitution of $x_k = \alpha_k$ in $B(f)$ cannot imply another ROBDD-based consequence.

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$$\begin{aligned}
 C(x_1, x_2, x_3, x_4) &= (x_1 \vee x_2) \cdot (\bar{x}_2 \vee x_3) \cdot (\bar{x}_3 \vee x_4) \\
 \swarrow_{x_1=0} & \\
 C|_{x_1=0} &= x_2 \cdot (\bar{x}_2 \vee x_3) \cdot (\bar{x}_3 \vee x_4) \implies x_2 = 1 \\
 \swarrow_{x_2=1} & \\
 C|_{x_1=0, x_2=1} &= x_3 \cdot (\bar{x}_3 \vee x_4) \implies x_3 = 1 \\
 \swarrow_{x_3=1} & \\
 C|_{x_1=0, x_2=1, x_3=1} &= x_4 \implies x_4 = 1
 \end{aligned}$$

(a) BCP



(b) ROBDD-case

Figure: The left part shows the BCP process in CNF $(x_1 \vee x_2) \cdot (\bar{x}_2 \vee x_3) \cdot (\bar{x}_3 \vee x_4)$ started by assigning $x_1 = 0$; the right part demonstrates the result of substituting $x_1 = 0$ into ROBDD representation of the considered CNF — here a single pass through the ROBDD is required.

Lazy computations

Consider the following conditions:

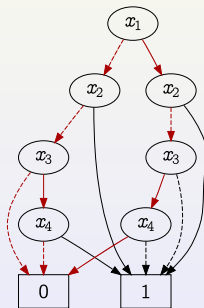
- 3 For a variable $x_q \in X$,
 $X = \{x_1, \dots, x_n\}$, every path π in $B(f)$ from the root to "0" passes through a vertex marked by x_q .
- 4 $|\Delta^0(x_q)| = 1$.

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Figure: ROBDD representation of a function $x_2 \vee (x_1 \oplus x_3 \cdot x_4)$ using the variable ordering $x_1 \prec x_2 \prec x_3 \prec x_4$. Conditions 3–4 hold for x_2 because each path from the root to "0" passes through a vertex marked by x_2 and $|\Delta^0(x_2)| = 1$.

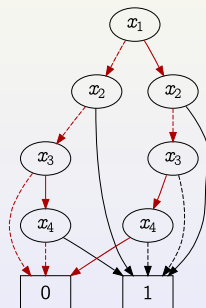


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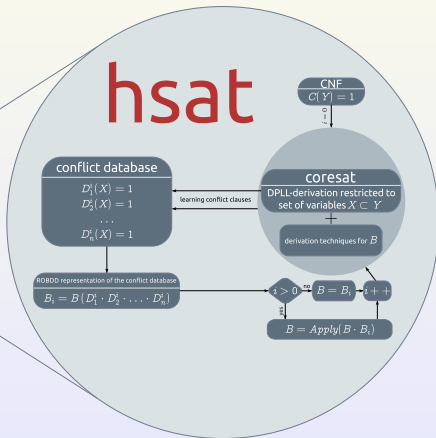
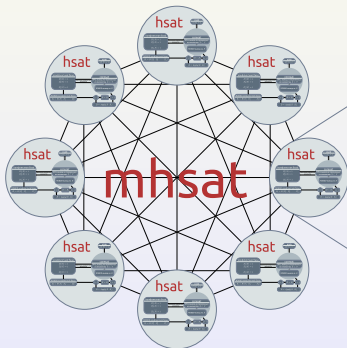
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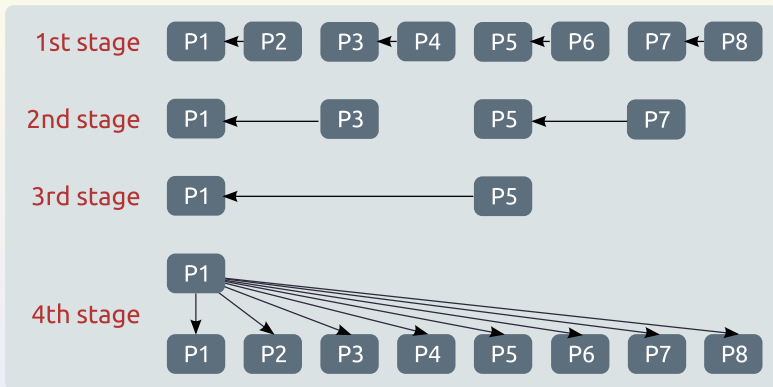


Theorem. Let $B(f)$ be an arbitrary ROBDD and there be such a variable x_q in $B(f)$ so that conditions 3–4 hold for x_q . Then there are no possible ROBDD-based consequences of any variable from $X \setminus \{x_q\}$ in $B(f)$. Time complexity of procedure which checks whether conditions 3–4 hold is $O(|B(f)|)$.

Parallel version of the hybrid solver (mhsat)



Simple exchange of conflict clauses



From MiniSat to mhsat

- **MiniSat-C v1.14.1** (<http://minisat.se/MiniSat.html>)

³See [Semenov, A., Zaikin, O., Bepalov, D., Posypkin, M.: Parallel algorithms for SAT in application to inversion problems of some discrete functions. arXiv:1102.3563v1 [cs.DC]].

⁴The core-DPLL implementation is based on the use of characteristic vectors similar to the technique proposed in [Kuehlmann, A., Paruthi, V., Krohm, F., Ganai, M.K.: Robust Boolean Reasoning for Equivalence Checking and Functional Property Verification. IEEE Transactions on Computer-Aided Design 21(12), 1377–1394 (2002)].

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- hsat (hybrid sat) = coresat + robdd-related procedures
- mhsat = n ·hsat + sharing accumulated conflict clauses through the MPI interface.

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A5/1 description

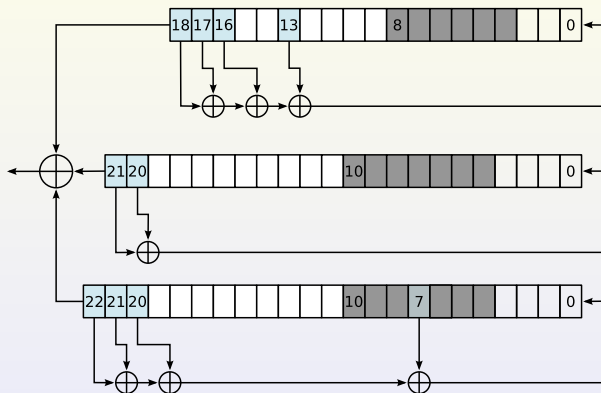


Figure: A5/1 keystream generator. Decomposition set X_{20} of 20 variables

The length of initial sequence is 64 bits.

$$\text{LFSR 1: } X^{19} + X^{18} + X^{17} + X^{14} + 1$$

$$\text{LFSR 2: } X^{22} + X^{21} + 1$$

$$\text{LFSR 3: } X^{23} + X^{22} + X^8 + 1$$

b_s^1, b_s^2, b_s^3 — values of the *clocking bits*

$$\chi_m(b_s^1, b_s^2, b_s^3) = \begin{cases} 1, & b_s^m = \text{majority}(b_s^1, b_s^2, b_s^3) \\ 0, & b_s^m \neq \text{majority}(b_s^1, b_s^2, b_s^3) \end{cases}$$

$$\text{majority}(x, y, z) = x \cdot y \vee x \cdot z \vee y \cdot z$$

Experiment description

- Test material: 50 CNFs chosen randomly from the decomposition family generated by decomposition set X_{20} .

⁵For each of 50 CNFs there constructed 4 simpler CNFs obtained by substituting all the possible values of two variables x_{23} and x_{45} into the original one.

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- Approaches:
 - Coarse-grained parallelization without sharing clauses (hsat, dminisat and MiniSat 2.2.0)⁵.
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- Experimental platform: Intel Xeon Processor E5345 (4 cores, 2.33 GHz), 8GB RAM.

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Experimental results

Table: Average solving time for each of the solvers.

place	solver	mode of operating	number of cores	avg. time (seconds)
1	mhsat ⁶	parallel	4	569.016
2	hsat	coarse-grained	4	644.254
3	MiraXT (mod)	parallel	4	1639.192
4	hsat	sequential	1	2385.578
5	dminisat	coarse-grained	4	2750.486
6	MiraXT (orig)	parallel	4	3214.178
7	ManySAT (mod) ⁷	parallel	4	3378.078
8	MiniSat (mod)	coarse-grained	4	5836.782

⁶mhsat taking 4 CPU cores is more than 4 times faster than its sequential version (hsat).

⁷Original versions of ManySAT and MiniSat cannot cope with the tests. However, one can improve them by assigning nonzero values to initial activity for those variables which correspond to initial contents of A5/1's registers.

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- Too few experiments.
- The core-DPLL+ROBDD derivation may be useful in inverting some discrete functions.
- Improvements of the approach are expected (both sequential and parallel versions).

Thank you for your attention!